

## IN THE SPECIFICATION:

Please replace paragraph [0019] with the following amended paragraph:

[0019] Generally, system 100 is configured to interface with a computing device motherboard 102 in lieu of a conventional graphics card and includes, without limitation, a graphics card 104 and an interface assembly 150. Graphics card 104 includes a GPU and a plurality of circuit components including memory (not shown) mounted to a first face 101, typically facing away from motherboard 102. Graphics card 104 further comprises a card connector 106 positioned along an edge 105 of graphics card 104 and adapted to engage interface assembly 150. Several embodiments of field changeable graphics cards suitable for use in rendering system 100 are described in co-pending, commonly assigned United States Patent Application No. 10/822,014~~-xx/xx,xx~~, filed 04/09/04~~-xx/xx,xx~~ by Bisson et al. (Attorney Docket No. NVDA/P001196), which is herein incorporated by reference.

Please replace paragraph [0026] with the following amended paragraph:

[0026] In one embodiment, an edge connector according to the present invention (e.g., edge connector 200) is adapted to detect a graphics mode of a computing device, and to cause display interfaces to be routed from a graphics card to the motherboard accordingly. Specifically, the PRSNT#1 connector pin on the edge connector (e.g., pin 134 in the pinout of FIGS. 3A-B) is adapted to detect if a graphics upgrade, such as any one of the graphics cards disclosed in the U.S. Patent Application No. ~~xx/xx,xx~~10/822,014, filed 04/09/04, has been implemented in the computing device. In one embodiment, a voltage detected by the PRSNT #1 connector pin indicates the presence of a graphics update. For example, a high voltage detected by the PRSNT#1 connector pin indicates that a "dummy" or "loop-through" card (e.g., a card with no graphics processing unit) is interfaced to the edge connector, as explained in further

detail in conjunction with FIGS. 6A and 7A below. Alternatively, a low voltage detected by the PRSNT#1 connector pin indicates that a graphics upgrade such as a graphics card is interfaced to the edge connector, as explained in further detail in conjunction with FIGS. 6B and 7B below.

Please replace paragraph [0031] with the following amended paragraph:

[0031] Alternatively, as illustrated in FIG. 6B, a manufacturer may close the circuit paths through resistors 614b and leave the circuit paths through resistors 614a open, in order to implement an active graphics card. In this embodiment, graphics system 600 comprises a graphics card 660 in place of loop-through card 650. Graphics card 660 may be configured in a manner similar to any one of the graphics cards described in U.S. Patent Application No. ~~xxxx,xxx~~10/822,014, filed 04/09/04, and includes discrete rendering device such as a graphics processing unit. Graphics card 660 generates substantially all display output signals, as described further below.